



Docket No. TI-32513.1

TIP515US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re PATENT application of:

Applicant: Manoj Mehrotra et al.  
Application No.: 10/691,284  
For: TRANSISTOR WITH BOTTOMWALL/SIDEWALL JUNCTION  
CAPACITANCE REGION AND METHOD  
Filing Date: October 22, 2003  
Examiner: Dilinh P. Nguyen  
Art Unit: 2814

**REPLY TO OFFICE ACTION DATED MARCH 22, 2006**

Mail Stop AF  
Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Favorable reconsideration of the above-identified application is respectfully requested in view of the following remarks.